

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,064,075 B2
APPLICATION NO. : 10/666918
DATED : June 20, 2006
INVENTOR(S) : Luigi Di Dio

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page: and Column 1.

Item (54) "METHOD FOR MANUFACTURING SEMICONDUCTOR ELECTRONICS DEVICES" should read as --METHOD FOR MANUFACTURING SEMICONDUCTOR ELECTRONIC DEVICES--

Column 6

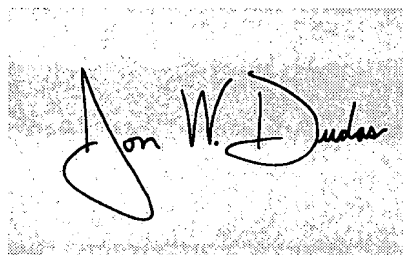
Line 40, "300 Åand 1600 Å" should read as --300 Å and 1600 Å--

Column 7

Line 16, "300 Åand 1600 Å" should read as --300 Å and 1600 Å--

Signed and Sealed this

Tenth Day of April, 2007

A handwritten signature in black ink, reading "Jon W. Dudas", is written over a rectangular area with a light gray, textured background.

JON W. DUDAS
Director of the United States Patent and Trademark Office